

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit device includes unit cells, memory cell blocks, bit lines, word lines, block select signal lines, plate lines, and a plate line driver. The unit cell includes a cell transistor and a ferroelectric capacitor connected between a source and a drain of the cell transistor. The memory cell block includes the unit cells connected in series between a first terminal and a second terminal and a block select transistor connected between the second terminal and a third terminal. The bit line connects commonly the third terminals of the blocks. The word line connects commonly gates of cell transistors in the blocks. The block select signal line connects commonly gates of block select transistors in the blocks. The plate line connects commonly the first terminals of the blocks. The plate line driver is connected to the plate lines and applies a potential to the plate lines.